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Re: U.S. Patent No. 7,449,908 B2; Issued: November 11, 2008
(Application No. 10/647,472; Filed: August 26, 2003)
For: **Process Monitor for Monitoring an Integrated Circuit Chip**
Inventors: BURNS *et al.*
Our Ref: 1875.3770001

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Request for Certificate of Correction under 37 C.F.R. § 1.322; and
2. One (1) page of Certificate of Correction Form (PTO/SB/44).

The above-listed documents are filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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JTH/LAG:mlb
Enclosures
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